TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT15Q102

High Power Switching Applications

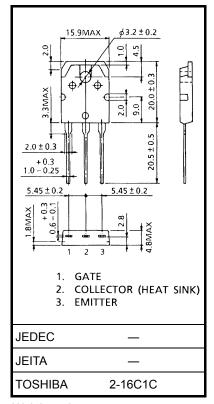
Unit: mm

- Third-generation IGBT
- Enhancement mode type
- High speed: $t_f = 0.32 \mu s \text{ (max)}$
- Low saturation voltage: VCE (sat) = 2.7 V (max)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	1200	V	
Gate-emitter voltage		V _{GES}	±20	V	
Collector current	DC	Ic	15	А	
	1 ms	I _{CP}	30		
Collector power dissipation		Pc	170	W	
(Tc = 25°C)		FC	170		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

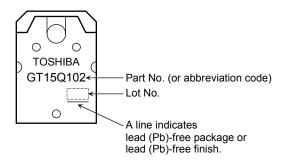
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.



Weight: 4.6 g

operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

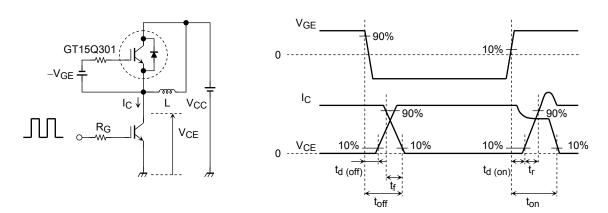
Marking



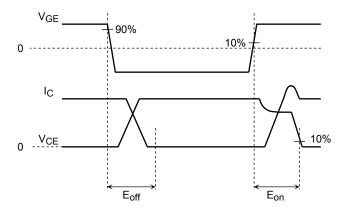
Electrical Characteristics (Ta = 25°C)

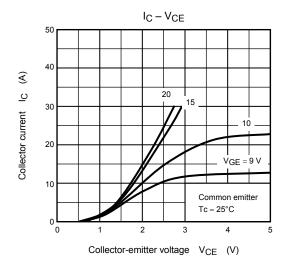
Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curre	ent	I _{GES}	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0$	_	_	±500	nA
Collector cut-off cu	ırrent	I _{CES}	V _{CE} = 1200 V, V _{GE} = 0	_	_	1.0	mA
Gate-emitter cut-of	ff voltage	V _{GE} (OFF)	I _C = 1.5 mA, V _{CE} = 5 V	4.0	_	7.0	V
Collector-emitter s	aturation voltage	V _{CE} (sat)	I _C = 15 A, V _{GE} = 15 V	_	2.1	2.7	V
Input capacitance		C _{ies}	V _{CE} = 50 V, V _{GE} = 0, f = 1 MHz	_	850	_	pF
Switching time	Rise time	t _r	Inductive Load	_	0.05	_	- μs
	Turn-on time	t _{on}	V _{CC} = 600 V, I _C = 15 A	_	0.12	_	
	Fall time	t _f	$V_{GG} = \pm 15 \text{ V}, R_G = 56 \Omega$	_	0.16	0.32	
	Turn-off time	t _{off}	(Note1)	_	0.56	_	
Thermal resistance	9	R _{th (j-c)}	_	_	_	0.74	°C/W

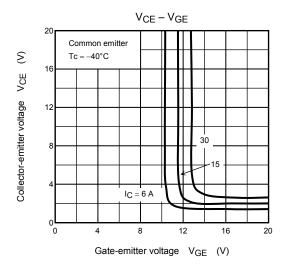
Note1: Switching time measurement circuit and input/output waveforms

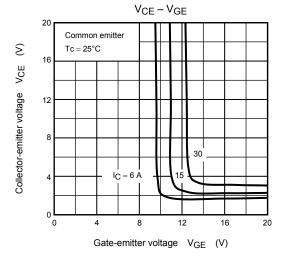


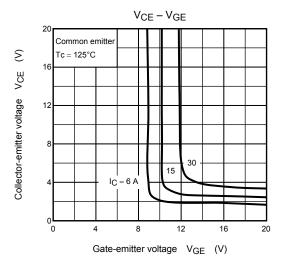
Note2: Switching loss measurement waveforms

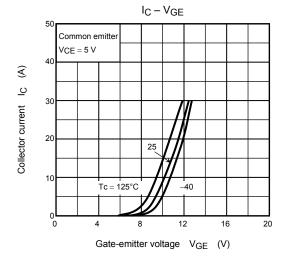


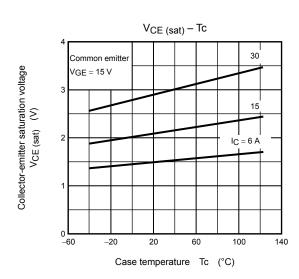




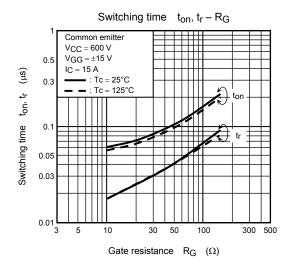


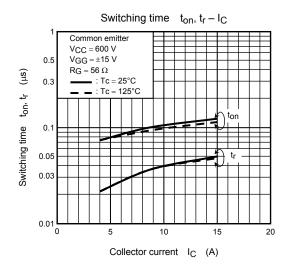


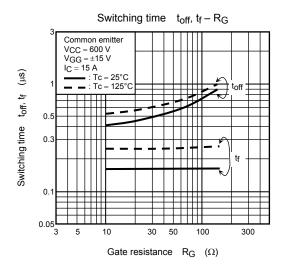


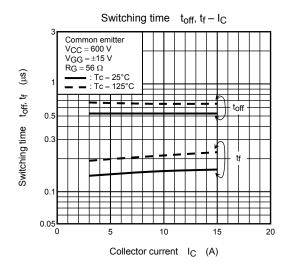


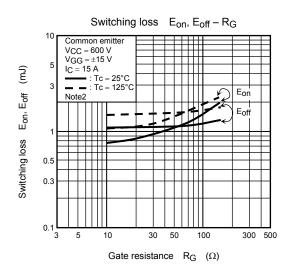
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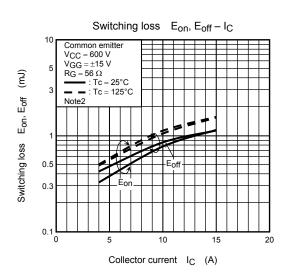


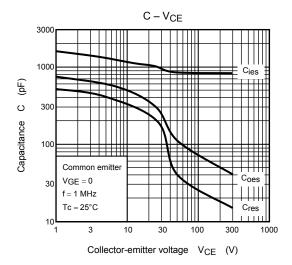


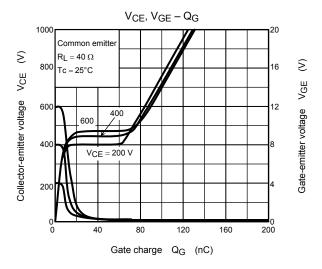


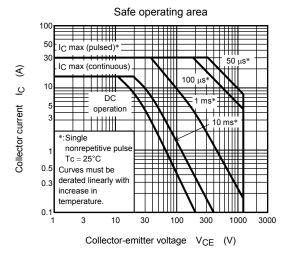


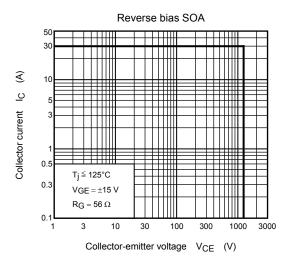


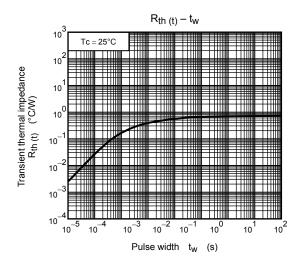












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